SMT-based Schedulability Analysis using RMTL-$\int$

André de Matos Pedro
CISTER/INESC TEC, ISEP
Rua Dr. António Bernardino de Almeida 431, 4200-072
Porto, Portugal
anmap@isep.ipp.pt

David Pereira
CISTER/INESC TEC, ISEP
Rua Dr. António Bernardino de Almeida 431, 4200-072
Porto, Portugal
dmrpe@isep.ipp.pt

Luís Miguel Pinho
CISTER/INESC TEC, ISEP
Rua Dr. António Bernardino de Almeida 431, 4200-072
Porto, Portugal
lmp@isep.ipp.pt

Jorge Sousa Pinto
HASLab/INESC TEC,
Universidade do Minho
Rua da Universidade
Braga, Portugal
jsp@di.uminho.pt

ABSTRACT

Several methods have been proposed for performing schedulability analysis for both uni-processor and multi-processor real-time systems. Very few of these works use the power of formal logic to write unambiguous specifications and to allow the usage of theorem provers for building the proofs of interest with greater correctness guarantees. In this paper we address this challenge by: 1) defining a formal language that allows to specify periodic resource models; 2) describe a transformational approach to reasoning about timing properties of resource models by transforming the latter specifications into a *satisfiability modulo theories* problem.

Categories and Subject Descriptors

C.3 [**Special-Purpose and Application-Based Systems**]: Real-time and embedded systems; D.2.4 [**Software Engineering**]: Software/Program Verification—*Formal methods, Model checking*; F.4.1 [**Mathematical Logic and Formal Languages**]: Mathematical Logic —*Temporal logic*

Keywords

Temporal logic, Schedulability analysis, Compositional, Hard Real-Time Systems, Embedded Systems

1. INTRODUCTION AND MOTIVATION

Very few works adopt formal logic as the framework for specifying and reasoning about the scheduling problems at hand. Therefore, specifications may be subject to multiple interpretations, and both the construction and checking of associated proofs becomes error prone. This is not the case when using formal logic, since the syntax and semantics must be defined unambiguously. Practitioners can use modern theorem provers to build machine checkable proofs of the unambiguous specifications that they are interested in showing for the scheduling analysis problem. Furthermore, (timed) temporal logic becomes capable to supply the synthesis algorithms with the scheduling problem that automatically outputs the concrete implementation via the transformation of the specifications into, *e.g.*, finite state machines.

In this paper we focus on the formal treatment of periodic resource models [6] with the goal of analyzing the compositionality of rigorously defined components, each one with its own set of real-time tasks and their associated timing properties. We transform the schedulability problem into a *satisfiability modulo theories* (SMT) problem in order to integrate the description of the scheduling behavior with the schedulability analysis. This allows to draw counter-examples when the system is not schedulable which can then be used for the system engineers to adapt the design accordingly.

1.1 Resource Models

As resource model (RM), we consider a model whose components are of two possible kinds, namely, *simple components* or *supervisor components*. A simple component is denoted by a tuple $C = (\Gamma, \omega, \vartheta, \phi)$ where $\Gamma = \{\tau_1, \ldots, \tau_n\}$ is a set of tasks, ω is a RM, ϑ is a scheduler policy, and ϕ is a set of properties defined in a *program logic* to monitor the behaviour of Γ. The supervisor components (or hypervisors) are tuples $\mathrm{H} = (\Omega, \phi_h)$ where Ω is a set of periodic resource models, and ϕ_h is a set of timed properties to check. Having these two kinds of components is justified by the fact that the framework was originally designed to be able to account for the specification and reasoning about runtime monitors as artifacts to check, upon run-time, that the RM behave as specified.

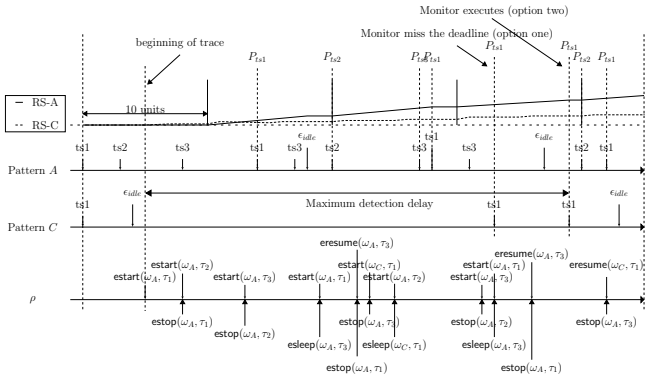

Figure 1: Example of patterns and the global trace generated by the composition of resource models.

Figure 1 shows an example of a concrete CMF instance. It considers two components RS-A and RS-C. The compontent RS-A consisting of ts1, ts2, and ts3; the component RS-C considers only a task, namely ts1. We can see in the Figure 1 two distinct patterns of execution, according to the task events estart, esleep, eresume, and estop, each denoting a job's status of execution (started, sleeping, resumed, and finished).

1.2 Adopted Formal Logic

For this work, we adopt the RMTL-$\int$ logic [4], a fragment of the MTL-$\int$ [3] with a restriction over the relations that can be defined at the term level. RMTL-$\int$ was introduce with the original aim of easing specification of periodic resource models and their verification/enforcement of properties during run-time. The syntax of RMTL-$\int$ is defined in a mutually inductive way. Let P and V denote, respectively, non-empty finite sets of propositions and variables. The terms denoted by η are of the form $\alpha \in \mathbb{R}$, $x \in V$, or $\int^{\eta} \varphi$. They correspond respectively, to a real-valued constant, a logic variable, and the duration of the formula φ. The formulae denoted by φ are of the form $p \in P$ (proposition), $\eta_1 < \eta_2$ (relation between terms), $\neg\varphi$ (negation), $\varphi_1 \vee \varphi_2$ (disjunction), $\varphi_1 \mathcal{U}_I \varphi_2$ (interval-bounded *until*), $\varphi_1 \mathcal{S}_I \varphi_2$ (interval-bounded *since*), or $\exists x\, \varphi$ (*existential* quantifier).

The semantical interpretation of RMTL-$\int$ formulas is defined elsewhere [4]. The model to interpret the formulas are sets of time-labelled traces produced by a periodic RM. As an example, we can use the RTML-$\int$ formula

$$\int^{10} \mathsf{estop}(\text{RS-A}(ts1)) < 9 \tag{1}$$

to denote that the task $ts1$ belonging to the resource model RS-A must hold in at most 9 time units in any execution trace before time 10 (see the time line Pattern A of the Figure 1).

2. SPECIFICATION OF RESOURCE MODELS

In order to allow for non-ambiguous specification of resource models and facilitate the construction of a RMTL-$\int$ formulae that has specifications of these models, we propose a simple language and transformation semantics. This language, named $\mathcal{L}$, has expressions to declare tasks and resource models, together with concurrency relations (higher priority or same priority between tasks and resource models). Let $\tau_1, \ldots, \tau_k$ be task names, $\rho_1, \ldots, \rho_l$ resource model names, $\mathsf{op}_t \in \{\succ, \bowtie\}$ and $\mathsf{op}_m \in \{\|, \gg\}$. The syntax of $\mathcal{L}$ is inductively defined by

$$tsk ::= \tau_i(C, P) \mid tsk_1\ \mathsf{op}_t\ tsk_2$$
$$rm ::= \rho_j(tsk, B, P) \mid rm_1\ \mathsf{op}_m\ rm_2,$$

where C is a WCET, tsk is a set of tasks, B and P are natural numbers denoting, respectively, a budget and period. The operator $\succ$ represents urgency among tasks, *i.e.*, if $tsk_1 \succ tsk_2$ holds then tsk_1 is a task with more urgency than tsk_2; the operator $\bowtie$ denotes that two tasks have exactly the same urgency in the system. Similarly, the operator $\gg$ denotes a urgency relation over resource models, and $\|$ denotes concurrent execution between two resource models with the same level of urgency in the system. For instance, a possible RM specification for Figure 1 can be expressed as

$$\text{RC-A}(ts1(10, 8) \succ (ts2(5, 20) \bowtie ts3(7, 27))) \;\|$$
$$\text{RS-C}(ts1(4, 33)).$$

The next step of our method consists in the transformation of a specification written in $\mathcal{L}$ into an equivalent RMTL-$\int$ specification. We can then check for the satisfiability of a scheduling property over the generated set of formulas, like for instance checking if task $ts1$ in RS-A gets more than 9 time units to halt, again using the Equation 1. Next, we convert this formula into the SMT-LIBv2[1] language using our tool [5] and delegate the reasoning to the Z3 solver [2].

```
(define-fun indicator ((mt Time)) Int
  (ite (= (compute_p trace mt p_a) TVTRUE) 1 0)
)

(declare-fun evaln ((Time)) Int)
(assert (= 0 (evaln 0))) (assert (forall ((x
    Int)) (=> (> x 0) (= (evaln x ) (+ (evaln (
    - x 1)) (indicator x) )))))

(assert (< (evaln 10) 9 ))
```

Listing 1: RMTL-$\int$ duration term encoding using SMT-Libv2.

To better exemplify how the process is done, let us assume the Listing 1 that shows an incomplete candidate encoding of the point-wise semantics for the RMTL-$\int$ duration term. The uninterpreted function $\texttt{compute}_p$ evaluates a proposition at the instant `mt`, and $\mathtt{p}_a$ is a proposition representing an event. It is true from the beginning of the event's occurrence until the next event is triggered in the system. Our goal is to find a trace (or set of traces) that satisfies these constraints, henceforth if the answer we obtain is *unsat* then the system is impossible to be scheduled (somehow the constraints may be incoherent); otherwise, we have a flow of the system for which these constraints result in a schedulable behaviour.

Comparatively to classic approaches, it is clear that this type of reasoning allows to construct and extend our constraints easily, instead of needing to reformulate every step of the analysis (it is a constructive approach). Note also that the expressiveness to deal with temporal order is of extremely

ID	Formula	Time	sat/unsat
(a)	$\Box_{<4}\ a \rightarrow \Diamond_2 b$	0.05s	✓
(b)	$\int^9 c < 2$	1.16s	✓
(c)	$((a \vee b)\ \mathcal{U}_{<10}\ c)$	0.59s	✓
(d)	$((a \vee b)\ \mathcal{U}_{<10}\ c) \wedge \int^9 c < 2$	1.38s	✓
(e)	$((a \vee b)\ \mathcal{U}_{<10}\ c) \wedge 10 < \int^9 c$	0.02s	unsat

Table 1: Preliminary results from Z3 SMT solver.

importance when dealing with systems depending on a time, which using just sets of inequalities and equalities alone cannot provide. It is therefore important to reuse such sets of (in-)equalities and combine them with logic connectives to get a fine-grained description of the system. Furthermore, the recent developments of SMT solvers positively impact our approach, namely due to the efficiency of the underlying reasoning methods that increases the chances of constructing the proofs we need in a fully automatic way.

3. PRELIMINARY RESULTS

Currently, it is not possible to devise a fair evaluation comparison for our approach since there are no available tools that consider duration terms in the way we consider in this work. In order to provide some insight about the feasibility of our technique, we have measured the times taken by the Z3 SMT solver to prove satisfiability of a set of specifications, as shown in Table 1. We have considered different structures for the presented formulae. The goal is to show indicators of the feasibility of the approach on sets of formulae with heterogeneous structural schemes, as we would expect to occur in a real-life example.

We noticed that time to solve formulas is not directly related with a formula's complexity or length, as formula (a) indicates. Note that formulas containing durations are slower in average to solve than formulas containing only temporal operators, as confirmed by the time it took to solve the satisfiability of formula (b) when compared to formula (c). Furthermore, a mix of both temporal operators and durations does not mean slower times as exhibited in the case of formula (d). Finally, we also have noted that showing that a formula is unsatisfiable is in general faster than proving satisfiability. The formula (e) from the Table 1 is an example of such phenomena.

More complex examples can be seen in the tool's repository [5]. Our experimental results indicate that this method can indeed be feasible for small sets of tasks and resource models.

4. CONCLUSION AND FURTHER WORK

In this paper we have described an alternative approach to scheduling analysis following a formal based rigorous specification of the components of the scheduling hierarchy, and its transformation into the SMTLIBv2 language for which we have used the Z3 solver to obtain valid schedules. Our plan in terms of future work is to improve on the developments done so far and on the kind of system we target, in order to understand how the proposal scales for systems which have characteristics very close to those used in the industry.

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